Analog.7042

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Zhang et al.

SERIAL NO:

10/620,395

FILED:

07/16/2003

FOR:

HIGH POWER, HIGH LINEARITY AND LOW INSERTION LOSS

GROUP:

2816

EXAMINER: Anh Q. Tran

SINGLE POLE DOUBLE THROW TRANSMITTER/RECEIVER

SWITCH

Mail Stop DD Commissioner

Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with 37 C.F.R. §§1.56, 1.97, and 1.98, Applicant submits copies of the documents listed on the attached Form PTO-1449.

The listed documents were recently cited in a corresponding PCT application, and a copy of the results of the Partial International Search Report is being submitted herewith for purposes of convenience.

The Commissioner is authorized to charge Deposit Order Account No. 19-0079 for any further fee that is required.

Respectfully submitted,

Matthew E. Connors

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Mail Stop DD, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Sarah E. Kennedy

Date: 15/65

(Rev. 5/92)

FORM PTO-1449 SAMUELS, GAUTHIER & STEVENS LLP 225 Franklin Street, Boston, MA 02110 Telephone: (617) 426-9180

> INFORMATION DISCLOSURE STATEMENT BY APPLICANT

7042

ATTORNEY DOCKET NO.

Zhang et al. APPLICANT

07/16/2003 FILING DATE 10/620,395 SERIAL NO.

2816 GROUP

Anh Q. Tran **EXAMINER**

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	ENT 8	TRADEMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AG	0766396	04/02/1997	ЕР			YES
	AH						
	ΑI						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL		·
	AJ	"Voltage-Tolerant Monolithic L-Band GaAs SPDT Switch," Cooper et al., IEEE MTT-S Digest (1989).
	AK	"A Dual-Gate Shorted-Anode Silicon-on-Insulator Lateral Insulated Gate Bipolar Transistor with Floating Ohmic Contact for Suppressing Snapback and Fast Switching Characteristics," Oh et al., Microelectronics Journal 30 pp. 577-581 (1999).
	AL	
	АМ	
	AN	
	AO	

EXAMINER

DATE CONSIDERED

EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.